

New Product Announcement

AP7343

AP7343 - 300mA High PSRR Low Noise LDO with Enable

The AP7343 is a Low Drop-Out regulator featuring high output voltage accuracy, high PSRR (requiring no bypass capacitor) with low output noise and low quiescent current.

With its low power consumption and, line and load transient response, the AP7343 is well-suited for low-power handheld communication equipment.

The AP7343 is available in fixed output voltage versions only with an enable pin. It is available with an automatic output discharge feature (D suffix) and also without (no D).

The AP7343 is packaged in the X2-DFN1010-4 package providing the smallest footprint and dense PCB layout. It is also available in SOT25, making it ideal for consumer and industrial applications in an industry standard pin out.



The Diodes Advantage

- Wide Input Voltage: 1.7V to 5.25V
 Offers operation down to 1.7V supporting operation over the whole range of 2 AA/AAA cells
- Wide Fixed Output Voltage Options: 0.9V to 3.6V in 100mV steps Wide output voltage options suitable for all applications
- High PSRR (75dB @ 1kHz) and Low Noise (60μVrms @ 10Hz 100kHz)
 Suitable for noise-sensitive applications such as audio, video and RF subsystems
- 1.0% Output Accuracy at Room Temperature, 1.5% over -40 to +85°C Maintains accuracy under all conditions
- Miniaturized Package, X2-DFN1010-4 (1.0mmx1.0mmx0.4mm) The small package dimensions and low height meets small footprint and low-profile requirements of the latest portable equipment
- Widely adopted industry standard Package SOT25
- Low Quiescent Current at 35μA with Enable Pin
 Offers power saving in low-power and battery operated products
 Enable pin with and without auto-discharge provides further battery savings
- With OTP and Current Limit Protection

Overtemperature protection and short-circuit current fold-back provides a ruggedized solution

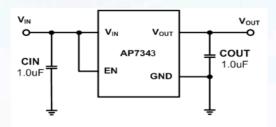
Applications

- Smart phone/tablets
- Portable video/media players
- Wireless adaptor/RF supply
- Cameras
- Wireless communication
- IoT

AP7343



Typical Application Circuit



Low Noise High PSRR LDO Portfolio

Part Number	V _{IN} (V)	V _{OUT1} (V)	I _{OUT} (mA)	V _{DROP} @ Full Load (mV)	Ι _Q (μΑ)	PSRR @1kHz (dB)	Noise (µVrms)	Output Accuracy (%)	Enable	BYPASS Cap	Package
AP7343x	1.7 to 5.25	0.9~3.6	300	300	35	75	60	1	Yes	No	X2-DFN1010-4 SOT25
AP7341x	1.7 to 5.25	1.2~3.3	300	300	35	75	60	1	Yes	No	X2-DFN1010-4
AP7315x	1.7 to 5.25	1.1~3.3	150	220	35	75	60	1	Yes	No	X2-DFN1010-4 SOT23, SOT25

