

InGaP HBT Active Bias Gain Block 50MHz to 6000MHz

## **RFMD SBB-4082S replacement** *a,b*

Criteria Labs CL1010 is a high-performance InGaP HBT MMIC amplifier utilizing a Darlington configuration with an active bias network in a hermetic package. The active bias network provides stable current over temperature and beta process variations. The CL1010 is designed for high linearity gain block military and industrial applications requiring excellent gain flatness, small size, minimal external components and hermetic packaging. Criteria Labs can provide various levels of device screening for military or highreliability space applications.



Package: Hermetic, 2-pin, 5.8mm x 2.8mm

**Product Features** 

- Single Fixed 5v Supply
- Ultra Flat Gain
- OIP3= 38dBm@ 1150MHz
- P1dB=19dBm @ 1150MHz

Applications:

- Military and Space Communications
- Industrial Applications
- Aerospace and Defense



Suitability for model replacement within a system must be determined and is solely the responsibility of the customer. а. The RFMD SBB-4082S part number is used for identification and comparison purposes only. b.

riteria labs

Criteria Labs 706 Brentwood St., Austin, TX 78752 www.criterialabs.com

512-637-4500

40-0010 Rev C

The information in this publication is believed to be accurate. However, no responsibility is assumed by Criteria Labs for its use, nor for any infringement of patents or other rights of third parties resulting from it use. No license is granted by implications or otherwise under any patent or patent rights of Criteria Labs. Criteria Labs reserves the right to change component circuitry, recommended application circuitry and specifications at any time without prior notice.

Page 1 of 5

Absolute Maximum Ratings		
Parameter	Rating	Units
Device Current (Id)	120	mA
Device Voltage *	6	V
RF Input Power	24	dBm
Junction Temperature (Tj)	150	С
Operating Temperature Range	-40 to 85	С
Storage Temperature Range	-55 to 150	С
Moisture Sensitivity Level	Hermetic	
ESD Rating - Human body Model (HBM)	Class 1C	
* For continuous use, do not exceed 5.2V.		

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

		CL1010 Specifications		ations	Conditions	
	Units	Min	typ	Max	Conditions	
General Performance					Test conditions: Vd = 5Vdc, Id = 82mA, OIP3 tone spacing 1 MHz, Pout per tone = 0 dBm TL=25C, 50 ohm system. Bias Tee tested.	
Small Signal Gain	dB	14	15.5	18	1.15 GHz	
	dB		15		4 GHz	
P1dB	dBm	18	19	22	1.15 GHz	
OIP3	dB	32	38		1.15 GHz, 1.151 GHz	
Input Return Loss	dB	11	20		1.15 GHz	
	dB		14		4 GHz	
Output Return Loss	dB	10	20		1.15 GHz	
	dB		10		4 GHz	
Reverse Isolation	dB		20		1.15 GHz	
Noise Figure	dB		4.5	6	1.15 GHz	
Operating Voltage	V		5			
Operating Current	mA	58	80	92		
Thermal Resistance	C/W		56		Junction to Case	



Criteria Labs 706 Brentwood St., Austin,<br/>www.criterialabs.comTX 78752S12-637-4500

40-0010 Rev C

The information in this publication is believed to be accurate. However, no responsibility is assumed by Criteria Labs for its use, nor for any infringement of patents or other rights of third parties resulting from it use. No license is granted by implications or otherwise under any patent or patent rights of Criteria Labs. Criteria Labs reserves the right to change component circuitry, recommended application circuitry and specifications at any time without prior notice.

Page 2 of 5

#### **Typical Performance**



The information in this publication is believed to be accurate. However, no responsibility is assumed by Criteria Labs for its use, nor for any infringement of patents or other rights of third parties resulting from it use. No license is granted by implications or otherwise under any patent or patent rights. Criteria Labs. Criteria Labs reserves the right to change component circuitry, recommended application circuitry and specifications at any time without prior notice.

Page 3 of 5

#### **Package Drawing**



Pin Names an		
Pin	Name	Description
1	RFIN	RF input pin. This pin requires the use of an external blocking
		capacitor chosen for the frequency of operation
2 RFC	RFOUT/DC Bias	RF output and bias pin. This pin requires the use of an external
		blocking capacitor and inductor chosen for the frequency of operation.
Package Paddle	GND	Package backside must be connect to RF/DC ground



Criteria Labs 706 Brentwood St., Austin, TX 78752 www.criterialabs.com 512-637-4500

40-0010 Rev C

The information in this publication is believed to be accurate. However, no responsibility is assumed by Criteria Labs for its use, nor for any infringement of patents or other rights of third parties resulting from it use. No license is granted by implications or otherwise under any patent or patent rights of Criteria Labs. Criteria Labs reserves the right to change component circuitry, recommended application circuitry and specifications at any time without prior notice.

Page 4 of 5

#### Typical Application Schematic







Criteria Labs 706 Brentwood St., Austin, TX 78752 www.criterialabs.com 512-637-4500

40-0010 Rev C

The information in this publication is believed to be accurate. However, no responsibility is assumed by Criteria Labs for its use, nor for any infringement of patents or other rights of third parties resulting from it use. No license is granted by implications or otherwise under any patent or patent rights of Criteria Labs. Criteria Labs reserves the right to change component circuitry, recommended application circuitry and specifications at any time without prior notice.

Page 5 of 5