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9P946AFLFT

Category: Zero Delay Buffers

Generic Part: 9P946

PC CLOCK Market Group: Description: PC BUFFER

Dual DDR zero delay buffer

Product Description/Features:

• High performance, low jitter zero delay buffer

• I2C for functional and output control

• Dual bank 1-6 differential clock distribution

· 2 separate feedback in & out for input to output synchronization for each bank

Supports up to 4 DDR DIMMs

• Supports up to 533MHz (DDRII 1066)

You may also like..



Parameters

Temperature	С
Voltage	3.3 V
Package	SSOP 48
Speed	NA
Status	Active
Sample	No
Minimum Order Quantity	1000
Factory Order Increment	1000

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Туре	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Package	SSOP 48 (PVG48)
Description	SSOP 300 MIL
Status	Active
Class	PLASTIC
Category	Green
Pb (Lead) Free	Yes
Mark	F
Package Type	SSOP
Lead Count	48
Length	15.9 mm
Width	7.5 mm
Thickness	2.3 mm
Pitch	0.64 mm
Tube / Tray	TUBE
Quantity per Tube/Tray	30
Quantity per Reel	1000
Moisture Sensitivity Level (MSL)	1
Moisture Exposure Floor Life	Unlimited@<30C/85% RH
Peak Reflow Temperature	260°C
Rebake Conditions	NA
Related Package Documents	view documents