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~	Changes from February 2,	2021 to Au		+ 8 2022 (fre	m Povision P	(Eobrus	arv 2	021) to	
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-	Global: Changed docum		n T A	15320E2833	(TM\$320E28)		al Si	anal Co	V
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	MicroStar BGA™ packag								
	Array (nFBGA) package.								
•	Global: Added 179-ball								
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•	Section 1 (Features): Ch								
•	Section 1: Added "179-ba								•
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•	Section 2 (Applications):	Updated se	CLIOI	Changed D		n takla	to D	okers !	nformation table
•	Section 3 (Description): L Added ZAY nFBGA to Pa								
	Table 5-1 (F2833x Device								
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 (SCI)"									
 Table 5-1: Added "179-Ball ZAY" to Packaging section. Added ZAY to "A" Temperature option									
	(SCI)"			•••					
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 Table 5-2: Added "179-Ball ZAY" to Packaging section. Added ZAY to "A" Temperature option									
Section 6.1 (Pin Diagrams): Added 179-ball ZAY new fine pitch ball grid array (nFBGA)									
Table 6-1 (Signal Descriptions): Added ZAY package									
•	Table 6-1: Updated DES								
•	Section 7.3 (ESD Rating	s – Comme	rcial): Add data fo	r ZAY package				3
•	ooonon nono (nooudonig								
Section 7.7.4 (ZAY Package): Added table									
 Section 7.9.2 (Power Sequencing): Updated "No requirements are placed on the power-up and power-down 									
	sequences" paragraph								
•	Section 7.9.5: Changed s "JTAG Debug Probe Con	section title	from	"Emulator C	onnection With	out Sign	al Bu	iffering f	for the DSP" to

•	Figure 7-27: Changed figure title from "Emulator Connection Without Signal Buffering for the DSP" to "JTAG
	Debug Probe Connection Without Signal Buffering for the MCU"
•	Figure 7-27 (Emulator Connection Without Signal Buffering for the MCU): Changed "DSC" to "MCU"
•	Section 7.9.6.8.2 (Synchronous XREADY Timing Requirements (Ready-on-Write, One Wait State)): Restored
	footnote
	Table 8-14 (SCI-C Registers): Restored footnotes
	Figure 8-15 (Serial Communications Interface (SCI) Module Block Diagram): Updated figure
	Figure 8-34 (Watchdog Module): Updated figure
	Section 9.1: Changed title from "TI Design or Reference Design" to "TI Reference Design"
	Section 9.1 (TI Reference Design): Updated section
	Section 10 (Device and Documentation Support): Updated section
	Section 10.1: Changed title from "Getting Started" to "Getting Started and Next Steps". Updated section 182
	Figure 10-1 (Example of F2833x, F2823x Device Nomenclature): Added 179-ball ZAY package under
	PACKAGE TYPE
•	Section 10.3 (Tools and Software): Updated section. Updated Design Kits and Evaluation Modules section.
	Updated Models section. Added Training section
•	Section 10.4 (Documentation Support): Added nFBGA Packaging Application Report
•	Section 10.4: Added Technical Reference Manual section
•	Section 10.4: Updated Peripheral Guides section. Removed most peripheral reference guides as they are
	now replaced by the TMS320x2833x, TMS320x2823x Real-Time Microcontrollers Technical Reference
	Manual
•	Section 11.1 (Package Redesign Details): Added section

The datasheet number will be changing.

Device Family	Change From:	Change To:
TMS320F2833x, TMS320F2823x	SPRS439P	SPRS439Q

These changes may be reviewed at the datasheet links provided.

https://www.ti.com/product/TMS320F28335

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Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:							
SA320F28335ZJZQ	SP320F28335ZJZQR	TMS320F28232PTPQ	TMS320F28234PTPQ				
TMS320F28234ZJZQ	TMS320F28235PTPQ	TMS320F28235ZJZQ	TMS320F28235ZJZQR				
TMS320F28335PTPQ	TMS320F28335ZJZQ	TMS320F28335ZJZQCR	TMS320F28335ZJZQR				

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Location	E-Mail			
WW PCN Team	<u>PCN ww admin team@list.ti.com</u>			

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